



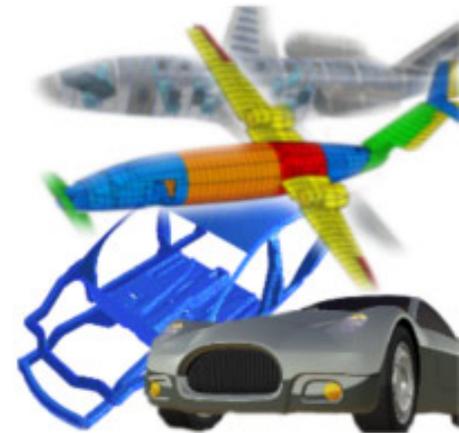
# Tuning tips for Optimal NX Nastran performance

Pari Rajaram  
High Performance Computing  
Hewlett-Packard



# NX Nastran

- Floating point intensive
  - cpu
  - memory system
- I/O intensive
  - Filesystem, controllers and disks.
- Distributed Memory Parallel (MPI)
  - Cluster
  - Large SMP servers



# Intel Itanium<sup>®</sup> 2 Processor



Memory Addressing ►

1024 TB

System Bus Bandwidth

6.4  
GB/s

On-die  
Cache ►

3-9 MB

Pipeline  
Stages ►

8

Issue Ports ►

1 2 3 4 5 6 7 8 9 10 11

On-die  
Registers ►

264 Application Registers  
+ 64 Predicate Registers\*\*\*

Execution Units ►

6 Integer,  
3 Branch

2 FP (FMAC)  
1 SIMD

2 Load and  
2 Store

Core Frequency ►

1.6 GHz

Instructions /  
Clk ►

6 Instructions / Cycle

EPIC Architecture



\*\*\* Intel's EPIC technology includes 64 single-bit predicate registers to accelerate loop unrolling and branch intensive code execution.

# HPC platforms

- Choice
- Performance
- Manageability



HP Integrity rx1620



HP Integrity rx2620



HP Integrity BL60p



HP Integrity rx4640



HP Integrity rx7620



HP Integrity rx8620



HP Integrity Superdome



HP ProLiant DL140 G2  
DL145 G2



HP ProLiant DL360 G4p  
DL385  
DL380 G4



HP ProLiant BL20p G3  
BL25p  
BL30p  
BL35p  
BL45p



HP ProLiant DL585



# Nastran job's memory requirement

- `nastran mem=estimate`
- After the job completion, look in the f04 file and see if the memory allocation was enough.

MINIMUM MEMORY REQUIREMENT = 6275 K WORDS

MEMORY AVAILABLE = 108480 K WORDS

MEMORY REQR'D TO AVOID SPILL = 62675 K WORDS

MEMORY USED BY BEND = 12235 K WORDS

HIWATER 123620969 WORDS

- Do not give more memory than required to run in-core.
- Give some of the remaining server memory for OS buffer cache or to nastran smem.
- Can allocate more than 8GB memory now using ILP64 option.

# Nastran Scratch files

- `nastran mem=estimate`

Estimated DOF=1584650

Estimated memory=730.3MB

Estimated disk=26591.2MB

- after the job completion, look in the `<job>.f04` file to see the actual file transfer and size

FILE	ALLOCATED (BLOCKS)	HIWATER	HIWATER (MB)	I/O TRANSFERRED (GB)
SCRATCH	250000	62253	15563.250	39.330
SCR300	250000	117099	29274.750	2311.260
				=====
			TOTAL:	2350.771

# Bring Elapsed Time closer to CPU Time

I/O bound

case1: Scratch file I/O to a /tmp single disk

real 3650 minutes:seconds.millisecond

user 2438

sys 320

I/O time =  $3650 - (2438 + 320) = 892$  seconds

case2: Scratch file I/O to a Raid disk (MSA30 7disks/1controller)

real 2895

user 2412

sys 286

I/O time =  $2895 - (2412 + 286) = 197$  seconds.

# General guidelines for RAID configuration

For good application performance:

- Large datasets
  - Every nastran job needs 4-5 disks per controller of I/O bandwidth.
  - Nastran throughput jobs: multiplies all this !
  - HP MSA20 - SATA disks JBOD 150MB/second



- HP MSA30 - Ultra 320 SCSI disks Raid 320MB/second



# HP Scalable File System (SFS) storage

## Business Need

- ✓ Balanced I/O bandwidth for computationally challenging science and engineering

## Storage Services

- ✓ Scalable bandwidth for technical computing

## Enabling Technology

- ✓ Distributed bandwidth
- ✓ Standard building blocks
- ✓ Single system image for 1000s of clients
- ✓ Pay as you grow scalability
- ✓ Open Source technology

HP StorageWorks  
Scalable File Share (HP SFS)

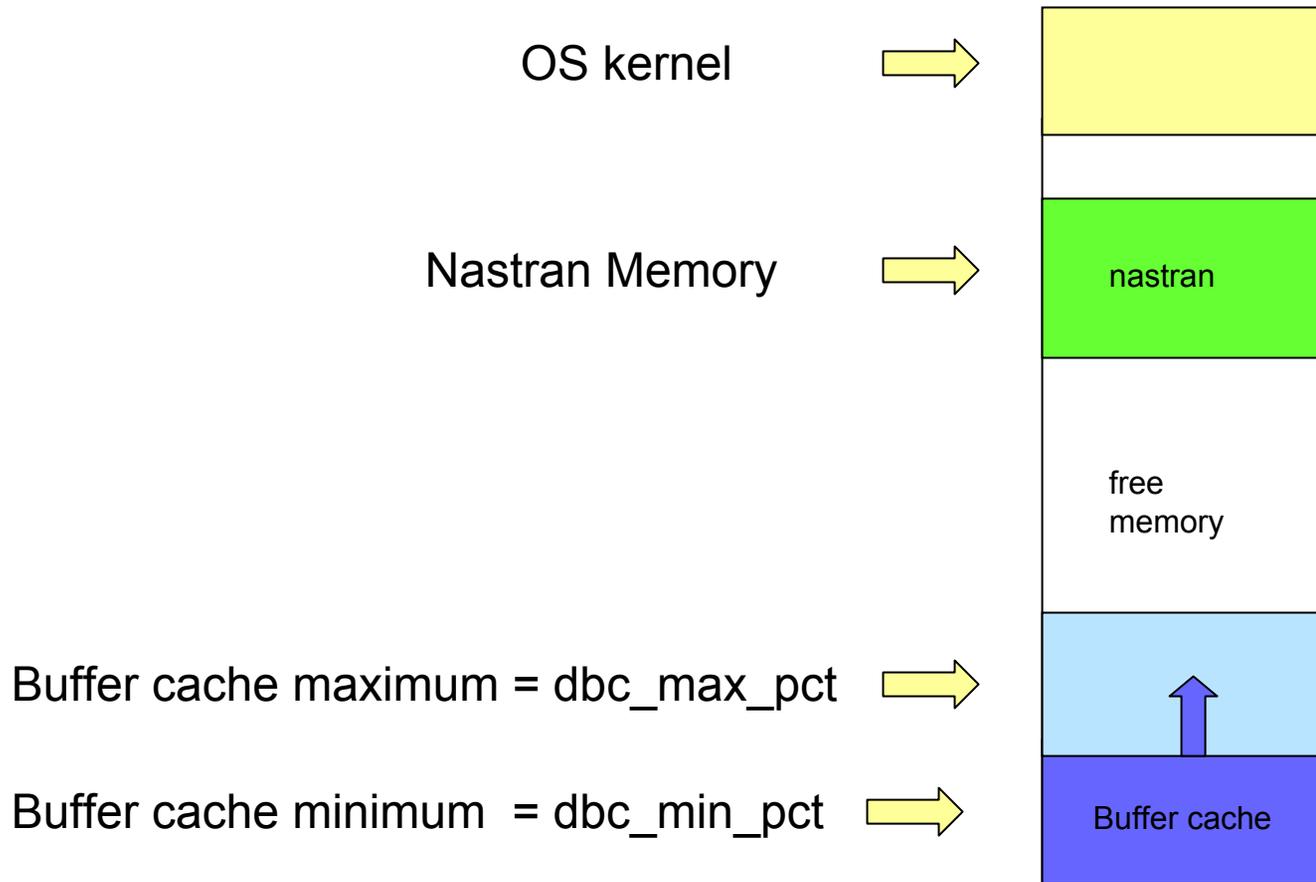


# OS Buffer cache

- Buffer Cache ( I/O )
  - Cache recently read/written I/O data for reuse in memory.
  - Used to hold 'read-ahead' and 'write-behind' data to increase i/o rates seen by an application.
  - Backup RAID I/O with decent amount of memory for buffer cache.
  - Leave room for Operating system and application.

# Tuning OS Buffer Cache

*hpux Tunables*



Physical Memory in the server

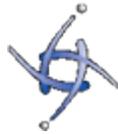
# Filesystem tuning

- Natran I/O pattern
  - More reads than write request
  - Majority sequential
  - Strided
  - Random
- `/etc/vx/tunefstab` file
  - $\text{Read\_ahead} = \text{read\_pref\_io} * \text{read\_nstream}$ 
    - $\text{read\_pref\_io} = 256\text{KB}$
    - $\text{read\_nstream} = \text{number of disks per controller}$
  - $\text{Write\_behind} = \text{write\_pref\_io} * \text{write\_nstream}$ 
    - $\text{write\_pref\_io} = 256\text{KB}$
    - $\text{write\_nstream} = \text{number of disks per controller}$

# NX Nastran

## + HP MPI

- support for wide array of Interconnects on HPUX and Linux



***Myricom***



- production quality MPI that scales 1000+ nodes.
- support from HP

## + HP Math library (MLIB)

- Support for HPUX and Linux
- 8 byte integer



HP: Platform of Choice for CAE™



**i n v e n t**

[www.hp.com/go/cae](http://www.hp.com/go/cae)